

## **SERVICE INFORMATION for SW upgrade with Service Pack for 81200 and 81250**

IMPORTANT: The installation of a Service Pack can only upgrade a specific SW rev.  
e.g.: **Service Pack 3.11SP1 can only upgrade an existing SW rev 3.11**

For further informations please do see :  
**"ParBERT 81250 Changes and Enhancement History"**  
**"81200/81210 Changes and Enhancement History"**

### **81250 rev 4.20 (20.02.2002)**

Upgrade installed SW rev. **4.20 to 4.20 SP3(12.03.2002)**

- E4862B: Pulse Port - Clock Mode: Delay field not updated in all conditions (Delay Control In on/off, Level Change)
- E4867A: Deskew does not work in Analyzer only systems where the E4808A Trigger output must be used as reference channel
- E4862B: Clock Skew inaccuracy because of internal missing update of crossing point DAC
- E4808A in Master / Slave configuration always shows self-test "Master Slave clock cable not connected"
- E4861B Module not mounted front-ends might generate feedbacks on the VXI bus. This for instance might cause immediate stop of the machine in capture mode.
- E4862B: Duty Cycle distortion erroneously available when Delay Control In is used. Now correctly disabled in GUI.
- E4861B some self-test messages in service mode cause the GUI to display a parser error message.

### **81250 rev 4.12 (13.12.2001)**

Upgrade installed SW rev. **4.12 to 4.12 SP1(09.01.2002)**

List of fixes:

- List of Firmware Servers is empty
- E4869A synchronization problem with ext clk
- E4869A CDR PLL cannot lock when switching between ext and int clk

### **81200/81250 rev 3.52 SP1(22.8.2001)**

Upgrade installed SW rev. **3.52SP1 to 3.52 SP2(26.10.2001)**

List of fixes:

- Trigger is wrong in external start mode (E4861A)
- Second 16 bits are doubled on external start (E4861A)

### **81200/81250 rev 3.52(30.7.2001)**

Upgrade installed SW rev. **3.52 to 3.52 SP1(22.8.2001)**

List of fixes:

- Module Selftest Errors might be shown at wrong Module (Problem introduced in 3.50 Release)
- some small memory leaks in firmware server when closing handles (Problem introduced with 3.00 Release)
- Sweep Delay does not reflect Frequency changes

### **81250 rev. 3.01(9.4.2001)**

Upgrade installed SW rev. **3.01 to 3.01SP1(3.5.2001)**

List of fixes:

- Netmeeting crash after ParBERT installation
- E4805b Selftest fails after measuring external frequency
- E4861A Fast Eye Mask fails on lower channel
- E4861A: timeouts too tight when measuring many bits
- E4861A & E4832A start/stop slower

### **81200 rev. 3.11(9.4.2001)**

Upgrade installed SW rev. **3.11 to 3.11SP1(3.5.2001)**

List of fixes:

- Netmeeting crash after ParBERT installation
- E4805b Selftest fails after measuring external frequency
- E4861A Fast Eye Mask fails on lower channel
- E4861A: timeouts too tight when measuring many bits
- E4861A & E4832A start/stop slower

## Software upgrade on embedded PC

In order to upgrade the current SW rev on an embedded PC it is helpful to optimize the PC speed.

- 1) Go to start ->RUN-> regedit->
- 2) HKEY\_LOCAL\_MACHINE \ SOFTWARE \ National Instruments \ NI-VXI For WIN32 \
- 3) **DisableMiteDMA** and change the value from **0** to **1**

See picture:

